

ENGINEERING CHANGE NOTIFICATION FORM

ECN: 80200558	REV: 1	ISSUE DATE: 11/17/2017	
TYPE OF CHANGE: Firmware Modification			
DETAILED DESCRIPTION OF CHANGE:			
Firmware release 1.12 for Low Noise Rubidium GN	NSDO for bug fixes and fu	nctionality improvements in Galileo support,	
phase normalization improvements and holdover p	performance improvement	S.	
REASON FOR CHANGE:			
Bug fixes and functionality improvements.			
PRODUCTS AFFECTED:			
Firmware Version		Model	
Firmware 0.36 and previous	Low Noise	Low Noise Rubidium GNSDO	
Notes:			
AVAILABILITY:			
MILESTONE		DATE	
ECN release for firmware release files		11/17/2017	
Release 1.12 for the Low Noise Rubidium C	GNSDO provides the fo	llowing improvements:	
Issue 1:			

uBlox receivers modified the GPS version command so it could no longer be read by the Low Noise Rubidium GNSDO. Units already in the field are not affected since this v1.12 is applied at the factory to units requiring the update.

Resolution:

Firmware 1.12 modifies the GPS message handling to support the modified GPS version command.

Issue 2:

uBlox receivers added support for Galileo but the GPS:SYST:SEL command in previous firmware versions did not support Galileo.

Resolution:

GPS:SYST:SEL command modified to support Galileo. Galileo is enabled with the GPS:SYST:SEL GAL command. The complete syntax of this command is:

GPS:SYSTem:SELect <GPS | SBAS | QZSS | GAL | BD ^ GLO>

Issue 3:

The Low Noise Rubidium GNSDO frequency phase stability during holdover depends greatly on the selection of holdover steering algorithm. Previous firmware releases did not take advantage of the long-term frequency stability of the Rubidium oscillator when calculating the holdover steering.

Resolution:

Version 1.12 adds an improved holdover steering algorithm that models the long-term rubidium oscillator behavior. The holdover steering algorithm also improves the transition from the short-term steering value to the long-term holdover

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steering to avoid negatively affecting short-term holdover performance.

Issue 4:

During a phase normalization the 1PPS output should be adjusted as close as possible to the reference 1PPS input while maintaining alignment with the 10MHz output clock (i.e.±50 ns). With previous firmware releases and under some conditions, this phase offset after a normalization could greater than 50 ns. This additional phase offset is removed by the disciplining algorithms, but starting with a higher offset results in a longer phase stabilization time.

Resolution:

Version 1.12 improves the phase normalization routine so that the initial phase offset is within approximately ±50 ns.

REFERENCE DOCUMENTS/ATTACHMENTS:

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